

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): M.A. Shibib et al.
Case: 38-7
Serial No.: To Be Assigned
Filing Date: September 30, 2003
Group: To Be Assigned
Examiner: To Be Assigned

Title: Metal-Oxide-Semiconductor Device Including
a Buried Lightly-Doped Drain Region

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 C.F.R. §§1.56, 1.97 and 1.98, Applicants' attorney wishes to bring to the attention of the Patent and Trademark Office the following documents listed on the accompanying Form PTO-1449. A copy of each listed document is enclosed, other than published U.S. patent documents.

U.S. Patent Documents

1. U.S. Patent No. 5,565,700 issued on 10/15/96 to Chou et al.

Other Documents

1. D.R. Disney et al., "A New 800V Lateral MOSFET with Dual Conduction Paths," Proceedings of International Symposium on Power Semiconductor Devices and ICs, 2001, pp. 399-402.

The filing of this Information Disclosure Statement shall not be construed as a representation that a search has been made, or as an admission that the information cited is considered to be material to patentability, or as a representation that no other material information exists.

Respectfully submitted,

A handwritten signature in black ink, appearing to read "Wayne L. Ellenbogen", with a long, sweeping horizontal stroke extending to the right.

Date: September 30, 2003

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FORM PTO-1449 (MODIFIED)**LIST OF PUBLICATIONS FOR
APPLICANT'S INFORMATION
DISCLOSURE STATEMENT**

Applicant(s): M.A. Shibib et al.
Case: 38-7
Serial No.: TBA
Filing Date: September 30, 2003
Group: TBA

U.S. PATENT DOCUMENTS

EXAMINER					FILING DATE
INITIAL	DOCUMENT NO.	DATE	NAME	CLASS/SUBCLASS	IF APPROPRIATE
_____	1. 5,565,700	010/15/96	Chou et al.		

FOREIGN PATENT DOCUMENTS

EXAMINER					TRANSLATION	
INITIAL	DOCUMENT NO.	DATE	COUNTRY	CLASS/SUBCLASS	YES	NO

OTHER DOCUMENTS

EXAMINER		
INITIAL	REF NO.	AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.
_____	1.	D.R. Disney et al., "A New 800V Lateral MOSFET with Dual Conduction Paths," Proceedings of International Symposium on Power Semiconductor Devices and ICs, 2001, pp. 399-402.

Examiner

Date Considered

Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.